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TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT  
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ABSTRACT:

PROBLEM TO BE SOLVED: To provide a semiconductor integrated circuit capable of rightly comparing phases at all the time without depending on the frequencies of clock signals by switching a delay step to be selected by a switch circuit, applying ratio information to an auxiliary circuit and performing control for matching the phases of reference clock and internal clock signal.

SOLUTION: Corresponding to the ratio information supplied from a control circuit 25, an interpolation circuit 24 generates the

internal clock signal of  
a phase having a transition edge between the transition  
edge of the first clock  
signal and the transition edge of the second clock signal.  
A phase comparator  
circuit 26 compares the phases of the reference clock  
signal and the internal  
clock signal. On the basis of the compared result of the  
phase comparator  
circuit 26, a control circuit 25 precisely controls the  
phase of the internal  
clock signal by applying the ratio information to the  
interpolation circuit 24  
so as to match the phases of the internal clock signal and  
the reference clock  
signal. Control due to the control circuit 25 is continued  
until the phases  
are matched.

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